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Dated: 10-15-03 Signature:

Docket No.: 21806-00113-US1 (PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Arne W. Ballantine, et al.

Application No.: 10/616,962

Group Art Unit: N/A

Filed: July 11, 2003

Examiner: Not Yet Assigned

For: METHOD OF REDUCING POLYSILICON **DEPLETION IN A POLYSILICON GATE**

ELECTRODE BY DEPOSITING

POLYSILICON OF VARYING GRAIN SIZE

REQUEST FOR CORRECTED FILING RECEIPT

Office of Initial Patent Examination Customer Service Center Commissioner for Patents Washington, DC 20231

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the aboveidentified patent application. The official Filing Receipt received by Applicant, a copy of which is attached hereto, has an error in the "Domestic Priority data as claimed by applicant" section. The Applicant has noted that the priority date for the Divisional application to which priority is claimed is incorrect. The "Domestic Priority data as claimed by applicant" should read as follows:

-- This application is as DIV of 09/802,702, filed 03/09/2001 --

A copy of the Application Data Sheet and First Preliminary Amendment filed on July 11, 2003 for the above-referenced application is also enclosed for your reference.

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Application No.: 10/616,962

Docket No.: 21806-00113-US1

As the error was made by the U.S. Patent and Trademark Office, Applicant believes that no fee is due with this request. However, if a fee is due, please charge our Deposit Account No. 22-0185, under Order No. 21806-00113-US1 from which the undersigned is authorized to draw.

Dated: October 15, 2003

12751_1

Respectfully submitted,

Larry J. Hume

Registration No.: 44,163

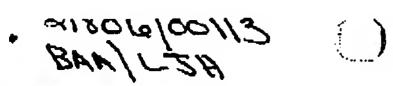
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS	
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30678 CONNOLLY BOVE LODGE & HUTZ LLP SUITE 800 1990 M STREET NW WASHINGTON, DC 20036-3425 CONFIRMATION NO. 2127
FILING RECEIPT

Date Mailed: 10/07/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Arne W. Ballantine, Round Lake, NY; Kevin K. Chan, Staten Island, NY; Jeffrey D. Gilbert, Burlington, VT; Kevin M. Houlihan, Boston, MA; Glen L. Miles, Essex Junction, VT; James J. Quinlivan, Essex Junction, VT; Samuel C. Ramac, Essex Junction, VT; Michael B. Rice, Colchester, VT; Beth A. Ward, Essex Junction, VT;

Assignment For Published Patent Application

International Business Machines, Armonk, NY;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/802,702-03/40/2004.* filed 03/09/2001 (*)Data provided by applicant is not consistent with PTO records.

Foreign Applications

If Required, Foreign Filing License Granted: 10/07/2003

Projected Publication Date: 01/15/2004

N n-Publication Request; No

Early Publication Request: No

Title

Method of reducing polysilicon depletion in a polysilicon gate electrode by depositing polysilicon of varying grain size

Preliminary Class

438

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